



2123

PATENT #2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/034,565  
Filed: October 19, 2001  
Inventor(s):  
Hugo A. Andrade, Brian Keith  
Odom and Arthur Ryan

Title: RECONFIGURABLE  
TEST SYSTEM

§ Examiner: Unknown  
§ Group/Art Unit: 2123  
§ Atty. Dkt. No: 5150-22503  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Jeffrey C. Hood

Name of Registered Representative

*Jeffrey C. Hood*  
Signature

June 27, 2002  
Date

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

**RECEIVED**

JUL 08 2002

Technology Center 2100

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☐ the additional information identified below in paragraph 3. A copy of each of the references listed on the Form PTO-1449 were previously submitted to or cited by the U.S. Patent and Trademark Office in U.S. Application Serial No. 09/229,695, issued as Patent No. 6,311,149, to which the captioned application is seeking priority under 35 U.S.C. §120. Copies of references, A1-A4, not previously submitted are enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);  
☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;

- ☒ before the mailing date of a first Office Action on the merits; or  
☐ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.
- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.
2. It is hereby certified:
- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.
3. ☐ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:
4. For each non-English language reference listed on the attached Form PTO-1449:
- ☐ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- ☐ reference is made to the concise explanation set forth below:
5. ☐ Applicant also offers the following comments for the Examiner's consideration:
6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8. ☐ Applicant(s) request that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

**CHANGE OF ADDRESS**

The Patent Office is respectfully requested to direct all future correspondence for this case to the below-referenced address.

If any required fees are missing, the Commissioner is authorized to charge said fees to Conley, Rose & Tayon, P.C. Deposit Account No. 03-2769/5150-22503/JCH.

Respectfully submitted,



Jeffrey C. Hood  
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Attorney for Applicant(s)

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P. O. BOX 398  
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Date: June 27, 2002

**Form PTO-1449** (modified)

List of Patents and Publications

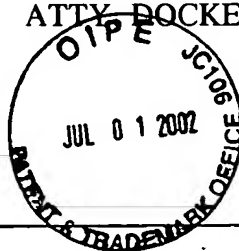
For Applicant's Information

Disclosure Statement

(Use several sheets if necessary)

ATTY. DOCKET NO: 5150-22503

SERIAL NO: 10/034,565



APPLICANT: Andrade et al.

FILING DATE: October 19, 2001

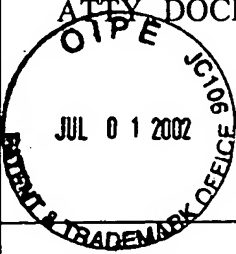
GROUP: 2123

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	6,230,307	05/082001	Davis et al.			
	A2	6,226,776	05/01/2001	Panchul et al.			
	A3	5,603,043	02/11/1997	Taylor et al.			
	A4	5,555,201	09/10/1996	Dangelo et al.			
		6,311,149	10/30/2001	Ryan et al.			
		4,901,221	2/13/90	Kodosky et al.			
		4,849,880	7/18/89	Bhaskar et al.			
		5,109,504	4/28/92	Littleton			
		5,784,275	7/21/98	Sojoodi et al.			
		5,437,464	8/1/95	Terasima et al.			
		5,566,295	10/15/96	Cypher et al.			
		5,684,980	11/4/97	Casselman			
		5,535,342	7/9/96	Taylor			
		5,652,875	7/29/97	Taylor			
		5,603,043	2/11/97	Taylor et al.			
		5,497,498	3/5/96	Taylor			
		5,583,749	12/10/96	Tredennick et al.			
		5,847,955	12/98	Mitchell et al.			

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
		WO 94 10627 A	5/11/94	PCT			
		WO 94 15311 A	7/7/94	PCT			
		WO 96 14618 A	5/17/96	PCT			

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY DOCKET NO: 5150-22503 		SERIAL NO: 10/034,565
	APPLICANT: Andrade et al.		
	FILING DATE: October 19, 2001	GROUP: 2123	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
	Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657.		
	Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214.		
	Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389.		
	Operating Systems, 2 <sup>nd</sup> Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415.		
	International Search Report for PCT/US 98/10916 dated 9/17/98.		
	"LabVIEW USER MANUAL for Windows", National Instruments Co., pp. 4/1-21.		
	Beguelin et al., "Visualization and Debugging in a Heterogenous Environment," IEEE, pp. 88-95.		
	Jamal et al., "The Applicability of the Visual Programming Language LabView to Large Real-World Application," IEEE, pp. 99-106.		
	Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems," IEEE, pp. 173-180.		
	XP0002087124 Xiao-Yu, et al., "Software Environment for WASMII: A Data Driven Machine with a Virtual Harware," Field Programmable Logic Architectures, Synthesis and Applications, 4 <sup>th</sup> International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin Germany, 1994.		
	XP000554820 Edwards, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, Vol. 143, No. 1, January 1996, pp. 55-63.		
	XP000380758 Lesser, et al., "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, Vol. 6, No. 2, August 1993, pp. 191-214.		
	International Search Report for PCT/US 98/13040 mailed 12/22/98.		
EXAMINER:		DATE CONSIDERED:	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.			